IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kevin J. Ryan	Examiner: James Peikari
Serial No.:	09/434,654	Group Art Unit: 2186
Filed:	November 5, 1999	Docket: 303.306US4
Title:	PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A UNIDIRECTIONAL COMMAND AND ADDRESS BUS AND A BIDIRECTIONAL DATA BUS		

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
Washington, D.C. 20231

This paper is in response to the Office Action mailed on September 9, 2002. Please amend the above-identified patent application as follows.

IN THE DRAWINGS

Enclosed is new Figure 6.

RECEIVED

OCT 1 7 2002

Technology Center 2100

REMARKS

Applicant refers to the outlined items in the Office Action.

Item 1(A)

Applicant herewith submits a further copy of the new drawing (Figure 6). Figure 6 was submitted in response to an objection stating that the drawings lack a data in buffer, a data out buffer, a column decoder, and a row decoder for the memory device (i.e., DRAM). Applicant herewith proposes a new Figure 6 showing these features. These features are part of the memory device as set forth in the specification. No new matter is believed proposed. Entry of new Figure 6 is requested.

Item 1(B)

The Office Action states that applicant has neither disputed nor filed a terminal disclaimer to overcome the double patenting rejection. Applicant respectfully submits that this statement misstates the status of this rejection. Specifically, this rejection is a *provisional*